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DFM Has Potential to Build Partnerships, But Will It?

By Ann Steffora Mutschler -- *Electronic News*, 2/8/2006

SANTA CLARA, CALIF. -- While not a new term by any account, design for manufacturing (DFM) is causing some of the biggest stir in the EDA industry for its promise to make chips that are more correct the first time around.

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During the DesignCon trade show here, a panel of executives representing EDA, r and the manufacturing arena gathered Tuesday to discuss the impact of this area of chain.

Dinesh Bettadapur, president and CEO of ASML Mask Tools said the integration mask and the wafer is greater than it has ever been, with this occurring across mas integration levels. Now, a static set of rules cannot capture the dynamic in the fab, previously, with a clear need existing for a process model to be fed upstream into process model must be then read into the relevant design steps, he explained.

Dr. Mike Smayling, CTO for the Maydan Technology Center group at Applied Masaid DFM is not new and it has been required since the beginning of IC design.

What is new, he said, is that lithography can't replicate what's drawn by designers problem is only getting worse. In addition, copper interconnect has more variabilit pattern sensitivity, than previous generation interconnect, combined with 2-D structure.

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being inaccurate as the real structures become 3-D.

Also, Smayling explained, designers and fab engineers speak different technical la tools needed to give systematic communications and translations.

The good news is that equipment companies like Applied can impact these issues tools for linking designs to the fab – the company's OPC-Check is one tool for tradesign "tags" to VeritySEM measurement sites on a wafer, then comparing the imoriginal GDS-II layout.

Applied is also looking to create equipment with better intrinsic uniformity such a tool that gives copper polish results with better within-wafer uniformity, along wit with Advanced Process Control, Smayling added.

Looking at the issue from another perspective, DFM is a behavior where yield is the effectivity, explained Mark Miller, VP for DFM business development at Cadence Systems, who also believes traditional DFM is inappropriately named. The real moto post-signoff manufacturing data optimization – where there is very little "D" in said.

Also, real DFM requires design implementation tools that proactively compensate manufacturing effects, where a new frontier involves cross-domain compensation manufacturing effects, Miller noted.

"If you are finding most of your DFM problems at signoff, the battle is almost lost continued.

A cousin to DFM is design for yield (DFY), which measures how well you did you you drill into the behaviors in the industry in years past, the contract was about sig sets and verification tools. The first big effect is that the contract is no longer who Manufacturers say, it is if you follow certain rules, and getting it close has been th Miller said.

The industry needs more proactive work done upstream. It is no longer good enouging off – it is a prerequisite that you understand manufacturing effects at various that understanding including yield issues and limiters impact the ramp to volume.

At Xilinx, Vincent Tong, VP of the company's FPGA products group, explained the company cut its DFM teeth on three FPGA generations of designs in 90nm and haproduced its first 65nm FPGA.

To achieve that, the programmable logic supplier maintains close collaborations w foundry partners, including UMC, to analyze test chip results to verify DFM, alon completing many simulations runs.

These partnerships have been built over time and are critical to the company's suc 65nm, Tong said.

Similarly, Applied's Smayling explained, "We have to keep in mind that everyone industry is trying to commoditize everyone else so they can be the one to product:

product. I think we will see strengthening partnerships to move the industry forwa

Dinesh Bettadapur, president and CEO of ASML MaskTools, noted that there is n company that can offer a complete DFM solution, which is why the equipment conbeen trying to figure out who to partner with.

In the area of DFM standards, Cadence's Miller pointed out that there is a paradox competitiveness and interoperability – there is always that first-mover advantage.

In this vein the role for standards is limited with regard to DFM, he believes.

Finally, the hottest topic concerning DFM comes down to where that data will con order to make truly easy to use and effective DFM tools a reality. Panelist Hamid of IBM's jointly developed Chartered/Samsung technology platform, explained Bitaking the approach of partnering with select vendors.

IBM has had end-to-end DFM capabilities for a long time and is currently coming solution that Chartered and Samsung can also use with their customers.

More partnerships will likely be formed in this way that combines the manufacturi how, process models and design implementation to deliver chips right the first tim



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